

ABSTRACT OF THE DISCLOSURE

A storage device controller includes a channel controller to receive a data input/output request sent from an information processor, a disk controller, and a cache memory. The channel controller includes a communication interface unit for the information processor, a data transfer unit connected via a first bus to the communication interface unit, and a processor connected via a second bus to the data transfer unit. The communication interface unit sends a read command to the data transfer unit for the processor. When the first bus conforms to a first communication protocol, the data transfer unit sends a split response to the communication interface unit. The data transfer unit sends the read command to the processor. The processor sends the split response and readout data corresponding to the read command to the data transfer unit. The data transfer unit receives and sends the readout data to the communication interface unit.